

Call for Papers

12th IEEE International Conference on Electronics, Circuits and Systems ICECS 2005

Gammarth, Tunisia, December 11-14, 2005

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The *IEEE International Conference on Electronics, Circuits and Systems (ICECS)* was founded in 1994. Since then it has become the most important annual event in the areas of electronics, circuits and systems within the IEEE Region 8, which includes Europe, Africa and Middle East. The 12th IEEE ICECS will be held in Gammarth, Tunisia on December 11-14, 2005. The conference will be organized by the Telecommunications School of Engineering (SUPCOM), Tunis. The ICECS is a major forum for scientists, engineers, and practitioners throughout the world to present their latest research results, ideas, developments, and applications in all areas of electronics, circuits and systems. ICECS 2005 will include keynote addresses, tutorials, exhibitions, special and poster sessions.

Topics include but are not limited to:

Analog Circuits and Signal Processing

- Analog circuits and filters
- Switched capacitor and switched current techniques
- Analog and mixed signal processing
- Data conversion and modulation
- Low-power and low-voltage circuits
- Radio frequency circuits
- Log-domain circuits

General Circuits and Systems

- Linear and nonlinear circuits and systems
- Chaos and applications
- Distributed circuits and systems
- Power electronics and systems

Digital Signal Processing

- Digital filters and filter banks
- Wavelets and multi-rate signal processing
- Adaptive signal processing
- Multidimensional systems
- Fast computations for signal processing
- Image indexing
- Stereovision

VLSI

- Analog and digital ICs
- Low power design
- VLSI Physical Design
- Testing: analog, digital and mixed
- High level synthesis and hardware/software co-design
- Logic synthesis and formal verification
- Fault tolerance systems
- Modelling, Simulation and CAD tools
- Microsystems and applications
- Processor architectures
- Deep sub/micron design

Communication & Network systems

- Wireless Communication Systems
- Optical Communication Systems
- Computer & Network Security Systems
- Network Architectures and Protocols

Computational Methods and Optimization

- Numerical methods and circuit simulation
- Linear and non-linear optimization
- Graph Theory and combinatorial optimization

Multimedia and Communications

- Speech processing and coding
- Image Processing and coding
- Video and multimedia technology
- Signal processing and communications
- Computer communications

Neural and Intelligent Systems

- Neural networks
- Cellular neural networks
- Fuzzy logic and circuits
- GIS and GPS systems and applications

Control and Power Systems

- Industrial automation
- Robotics
- Control and Power systems and applications

Industrial, Biomedical and Educational Applications

- Sensors and interfaces
- Space Systems Design
- Micromechanics
- Electronic Education
- Web-based education
- Distance education

Digital Communications

- Information Theory
- Coding and Modulation
- Turbo Codes and Iterative Decoding
- Equalization, Synchronization and Channel Estimation
- Multi-User Detection
- Antenna Arrays
- MIMO Systems
- Space-Time Coding and Processing
- Multiple Access Techniques
- Diversity Techniques

Authors who wish to participate in ICECS 2005 should submit 4 page-papers (using 10 point-font) addressing theories, methodologies, and applications related to the aforementioned topics. This is the maximum number of pages that will be allowed including tables, figures, and references.

All papers will be peer reviewed based on full-paper submission. Acceptance will be based on quality, relevance and originality. Accepted papers will be published in the conference proceedings.

Papers must be submitted (in Word, PDF, or PS format) by using the following web site: www.icecs2005.rnrt.tn

Paper Submission Deadlines

Full Paper Electronic Submission:
Notification of acceptance/Rejection:
Camera ready submission of full papers:

June 20, 2005
September 15, 2005
October 15, 2005